

MAY 20 2004

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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. .... 10/627,468  
Filing Date ..... July 25, 2003  
Inventor ..... Tyler A. Lowery et al.  
Assignee ..... Micron Technology, Inc.  
Group Art Unit ..... 2811  
Examiner ..... Unknown  
Attorney's Docket No. .... MI22-2348  
Title: Methods of Forming Capacitors, Methods of Forming Capacitor-Over-Bit Line  
Memory Circuitry, and Related Integrated Circuitry Constructions

Assistant Commissioner for Patents  
P.O. Box 1450  
Alexandria VA 22313-1450

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1. Supplemental Information Disclosure Statement with Form PTO-1449
2. Cited References (4)
3. Transmittal Form (PTO/SB/21)

Dated: 5/20/2004By: 

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NUMBER OF PAGES IN FACSIMILE: 45

PTO/SB/21 (02-04)

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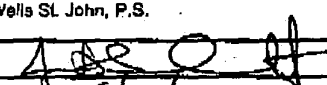
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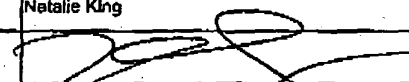
<b>TRANSMITTAL FORM</b>  <i>(to be used for all correspondence after initial filing)</i>	Application Number	10/627,488
	Filing Date	July 25, 2003
	First Named Inventor	Tyler A. Lowary et al.
	Art Unit	2811
	Examiner Name	Unknown
	Attorney Docket Number	MI22-2348
Total Number of Pages In This Submission		

ENCLOSURES (Check all that apply)		
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Remarks		

## SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm or Individual name	James D. Shauratte, Reg. No. 39,833 Wells St John, P.S.
Signature	
Date	5/19/04

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Typed or printed name	Natalie King
Signature	
Date	5/20/2004

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SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

References -- See Attached Form PTO-1449

The attached form PTO-1449 is submitted in compliance with 37 CFR §1.56. No admission is made regarding whether all the submitted references are prior art.

Respectfully submitted,

Dated: 5/19/04

By: 

James D. Shaurette  
Reg. No. 39,833

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